

DETAILED ACTION

1. This Office Action is in response to the amendment filed on 1/15/2007. Claims 1-21 are pending.

Allowable Subject Matter

2. After further search and thorough examination of the present application and in view of the prior art of record, claims 1-21 are found to be in condition for allowance.

Reason for Allowance

The following is an examiner's statement of reasons for allowance: The present invention relates to techniques for updating cyclic redundancy check bytes in a data storage system, and more particularly, to techniques for updating cyclic redundancy check bytes to correspond to a reassigned logical block address.

To achieve the inventive goal, independent claims 1 and 8 identify the distinct features "a cycle redundancy check (CRC) engine that generates old cyclic redundancy check bytes based on the old LBA, performs a first exclusive OR (XOR) function on the old LBA and the new LBA, performs Galois Field multiplication on a result of the first XOR function, and performs a second XOR function on a result of the Galois Field multiplication and the old cyclic redundancy check bytes to generate updated cyclic redundancy check bytes that are based on the new LBA."

Independent claim 15 identifies the distinct features "circuitry for performing Galois Field multiplication on a result of the first XOR function; and a second XOR gate for performing a second XOR function on a result of the Galois Field multiplication and the old cyclic redundancy check bytes to generate updated cyclic redundancy check bytes that are based on the second LBA."

The first cited reference, Malakapalli (U.S. Pat. No. 6,467,060) relates to data integrity and error detection and correction in a disc drive. The second cited reference, Frederickson (U.S. Pat. No. 5,805,799) discloses data integrity and cross-check code with logical block address.

However, the features disclosed in the instant application (No. 10/671,189) are neither anticipated nor render obvious by the prior art of record.

These limitations, in conjunction with all other limitations of the base claims were not shown by, would not have been obvious over, would not have been fairly suggested by the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any response to this action should be mailed to:

Commissioner of Patents and Trademarks, Washington, D.C. 20231

or faxed to: (703) 872-9306 for all formal communications.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fritz Alphonse, whose telephone number is (571) 272-3813. The examiner can normally be reached on M-F, 8:30-6:00, Alt. Mondays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jacques Louis-Jacques, can be reached at (571) 272-6962.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-3824

Information regarding the status of an application may also be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Fritz Alphonse /

Art Unit 2112

4/9/2008

/Mujtaba K Chaudry/
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